



IMPROVED SIGNAL ROUTING BETWEEN A MEMORY
CONTROL UNIT AND A MEMORY DEVICE

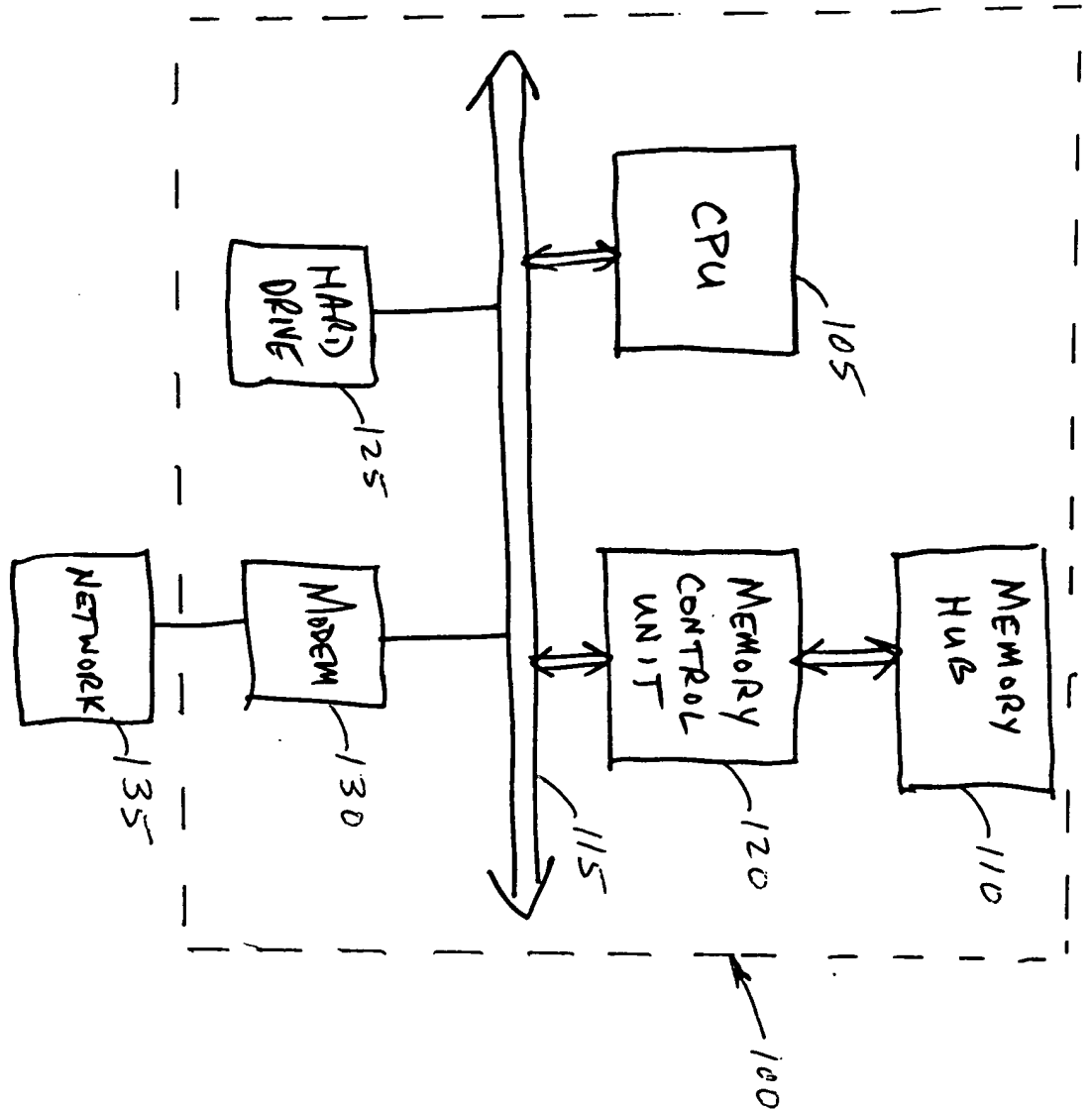


FIG. 1 PRIOR ART

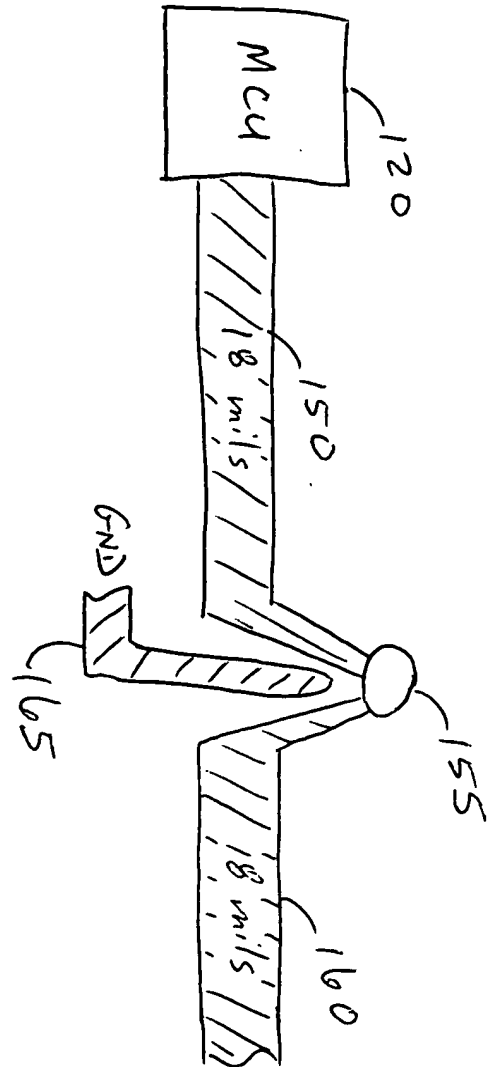


FIG. 2 PRIOR ART

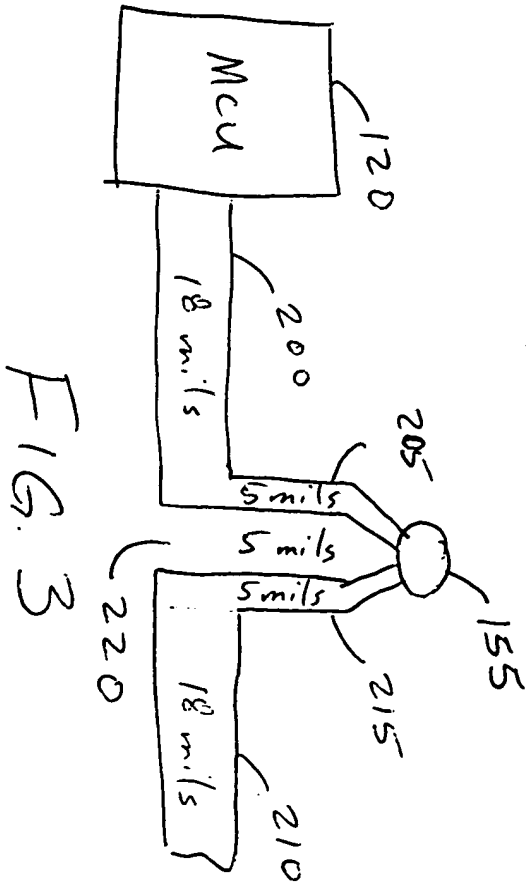


FIG. 3

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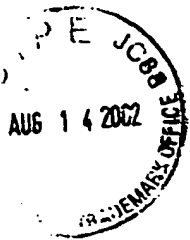


FIG. 4

